IEEE SiPS 2024
IEEE Workshop on Signal Processing Systems

4–6 November 2024
Cambridge, Massachusetts, U.S.A.

The 37th IEEE Workshop on Signal Processing Systems (SiPS) is a premier international forum in the area of design and implementation of signal processing systems. It addresses all aspects of architecture and design methods of these systems. Emphasis is on current and future challenges in research and development in both academia and industry.

Program Highlights

Software Implementation of Signal Processing Systems

Hardware Implementation of Signal Processing Systems

Design Methods of Signal Processing Systems

Machine Learning for Signal Processing

Signal Processing Application Systems

Special Sessions

- Quantum techniques in signal processing and machine learning
- Signal processing in communications, navigation and sensing for autonomous systems
- Technologies for next-generation edge AI platforms

Keynote Speakers

Song Han, MIT

William Oliver, MIT

Alexander Wyglinski, WPI

Important Deadlines

Student and Young Professional
Funding Application — Sep 30, 2024
Advanced Registration — Oct 7, 2024

Committee

General Chairs
John McAllister, QUB
Joe Cavallero, Rice

Program Chairs
Jari Nurmi, Tamere Univ.
Jani Boutellier, Univ. of Vaasa

Finance Chair
Warren Gross, McGill University

Local Organizing Chair
Brian Telfer,
MIT Lincoln Laboratory

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